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63519

Application ID:

10749103

Title of Invention:

LAYER SEMICONDUCTOR

STRUCTURE INCORPORATING A

METHOD OF FORMING A MULTI-

PROCESSING HANDLE MEMBER

First Named Inventor:

Rafael Reif

Domestic/Foreign Application: Domestic Application

Filing Date:

2003-12-30

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Statement

Filing Type:

Confirmation number:

3165

Attorney Docket Number:

MIT-136BUS

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TRANSMITTAL

Electronic Version v1.1 Stylesheet Version v1.1.0

> Title of Invention

METHOD OF FORMING A MULTI-LAYER SEMICONDUCTOR STRUCTURE INCORPORATING A PROCESSING HANDLE **MEMBER**

Application Number:

10/749103

Date:

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First Named Applicant:

Rafael Reif

Confirmation Number:

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Attorney Docket Number: MIT-136BUS

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Submitted by:	Elec. Sign.	Sign. Capacity	
Christopher S Daly Registered Number: 37,303	/csd/	Attorney	

Documents being submitted

Files

us-ids

MIT-136BUS-usidst.xml

us-ids.dtd

us-ids.xsl

Comments



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

Title of Invention

METHOD OF FORMING A MULTI-LAYER SEMICONDUCTOR STRUCTURE INCORPORATING A PROCESSING HANDLE MEMBER

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Search string:

(4313126 or 4402761 or 4456888 or 4599704 or 4986861 or 5156997 or 5206186 or 5445994

or 5821138 or 5923087 or 6441478).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4313126	1982-01-26	Krumm et al.			
	2	4402761	1983-09-06	Feist			
	3	4456888	1984-01-26	Ayasli			
	4	4599704	1986-07-08	Mazin			
	5	4986861	1991-01-22	Nishida et al.			
	6	5156997	1992-10-20	Kumar et al.			
	7	5206186	1993-04-27	Neugebauer et al.			
	8	5445994	1995-08-29	Gilton			
	9	5821138	1998-10-13	Yamazaki et al.			
	` 10	5923087	1999-07-13	Suzuki et al.			
	11	6441478	2002-08-27	Park			

Signature

Examiner Name	Date		